

FIG. 1 A

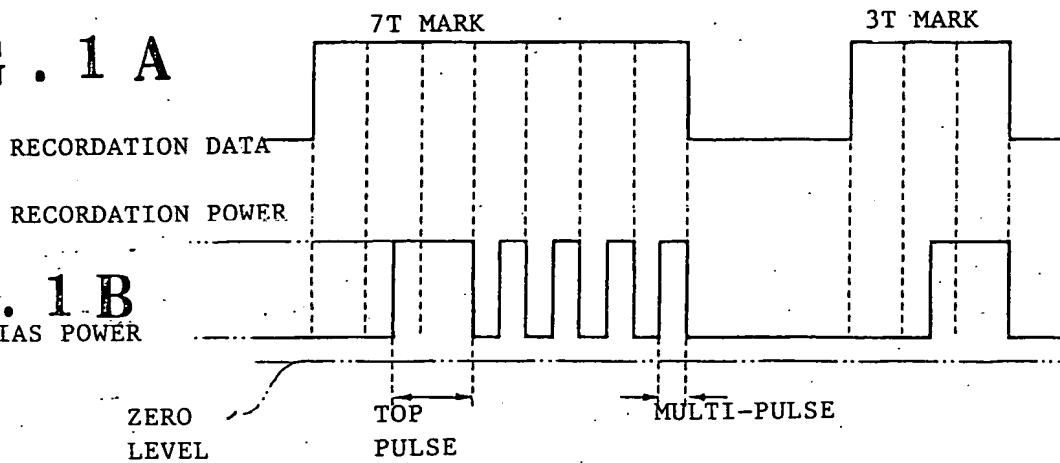


FIG. 1 B

BIAS POWER

ZERO LEVEL
TOP PULSE
MULTI-PULSE

FIG. 2 A

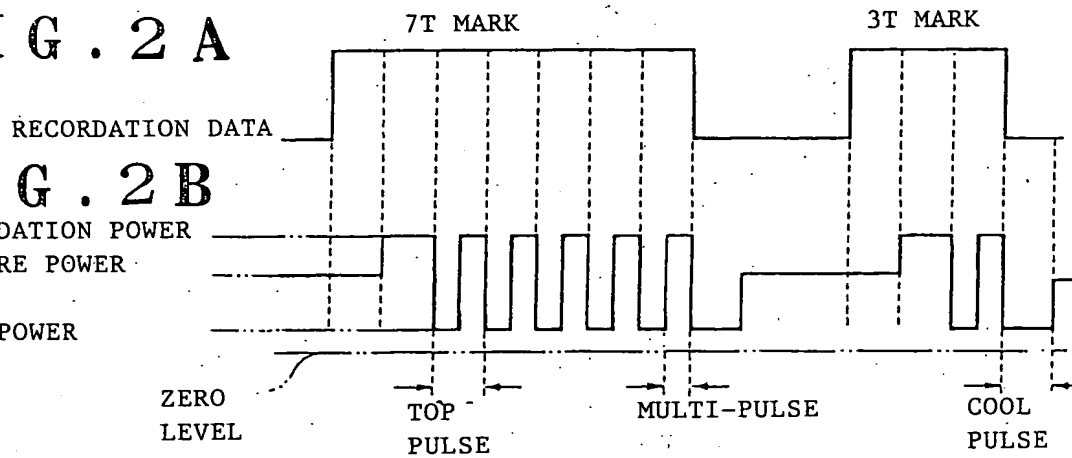


FIG. 2 B

RECORDATION POWER
ERASURE POWER

BIAS POWER

ZERO LEVEL
TOP PULSE
MULTI-PULSE
COOL PULSE

FIG. 3A

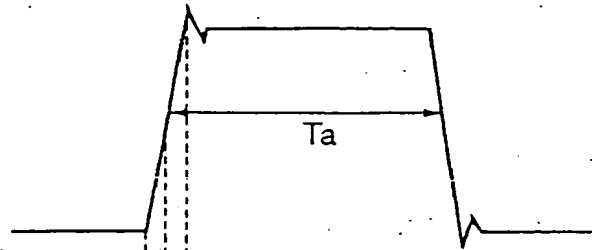


FIG. 3B

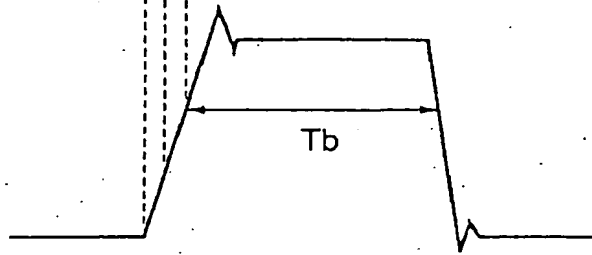


FIG. 4A

WRITE PULSE

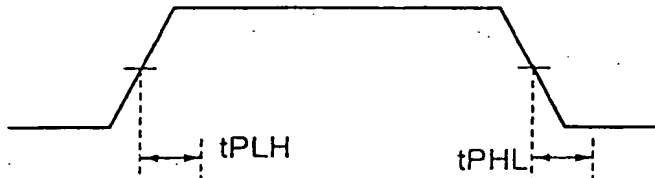


FIG. 4B

IN PHASE OUTPUT

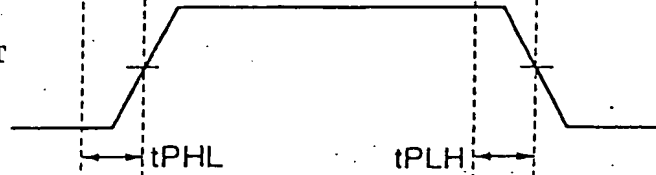
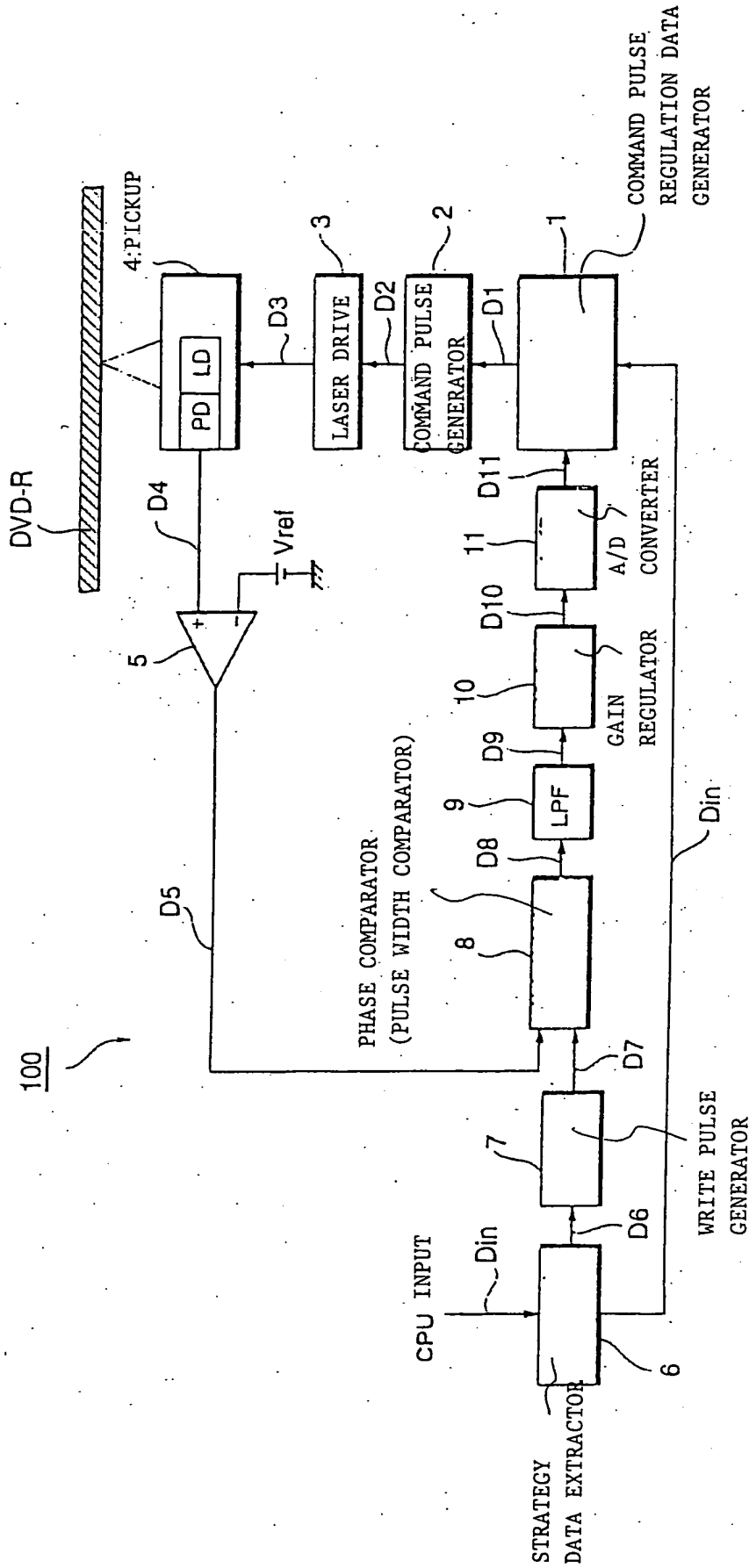


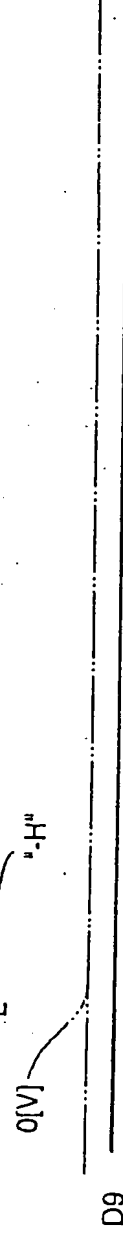
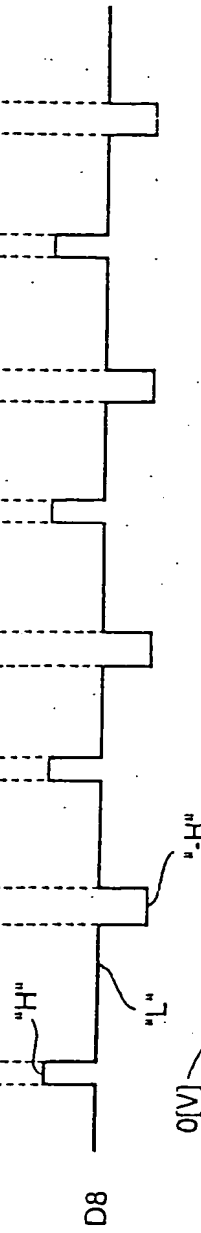
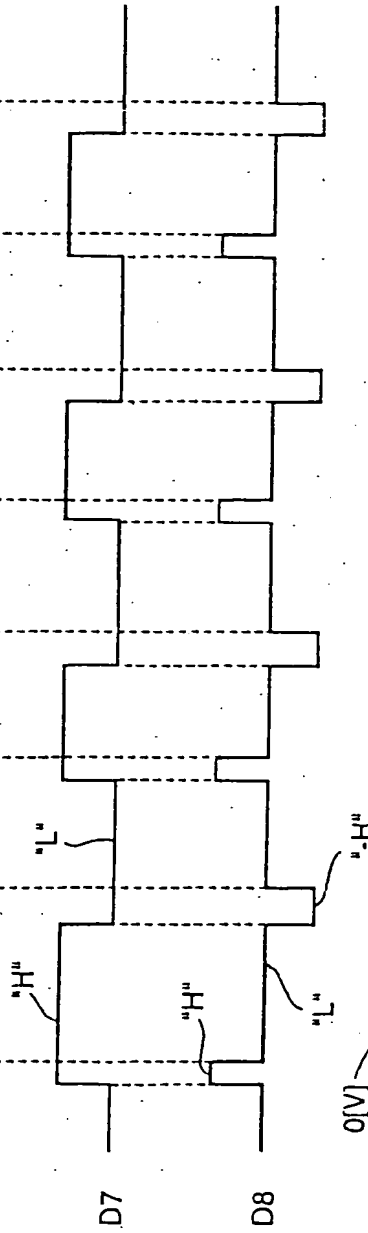
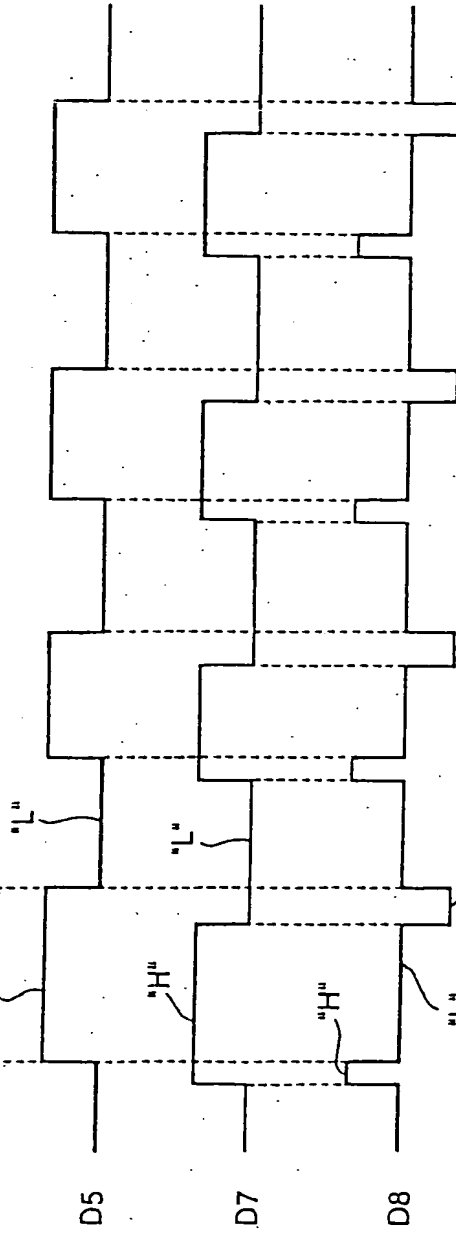
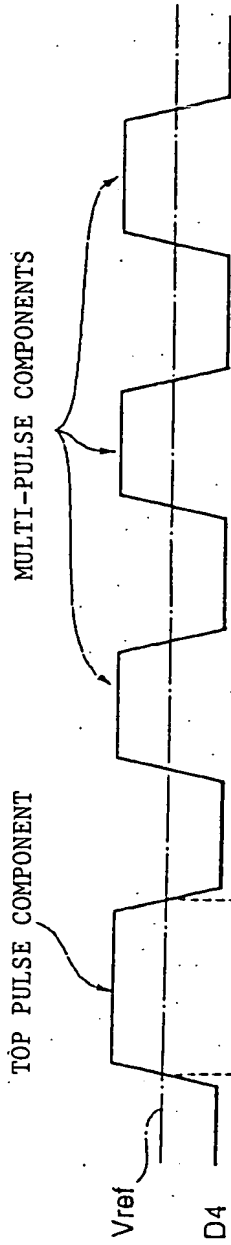
FIG. 4C

INVERTED OUTPUT



FIG. 5



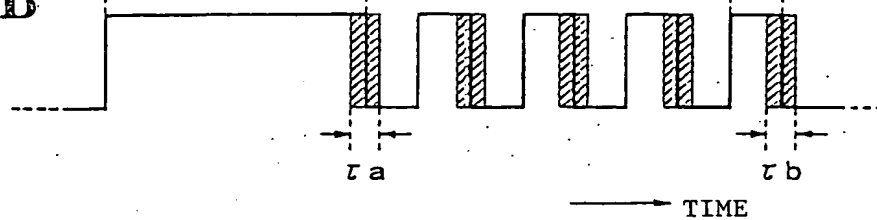


TIME

FIG. 7 A

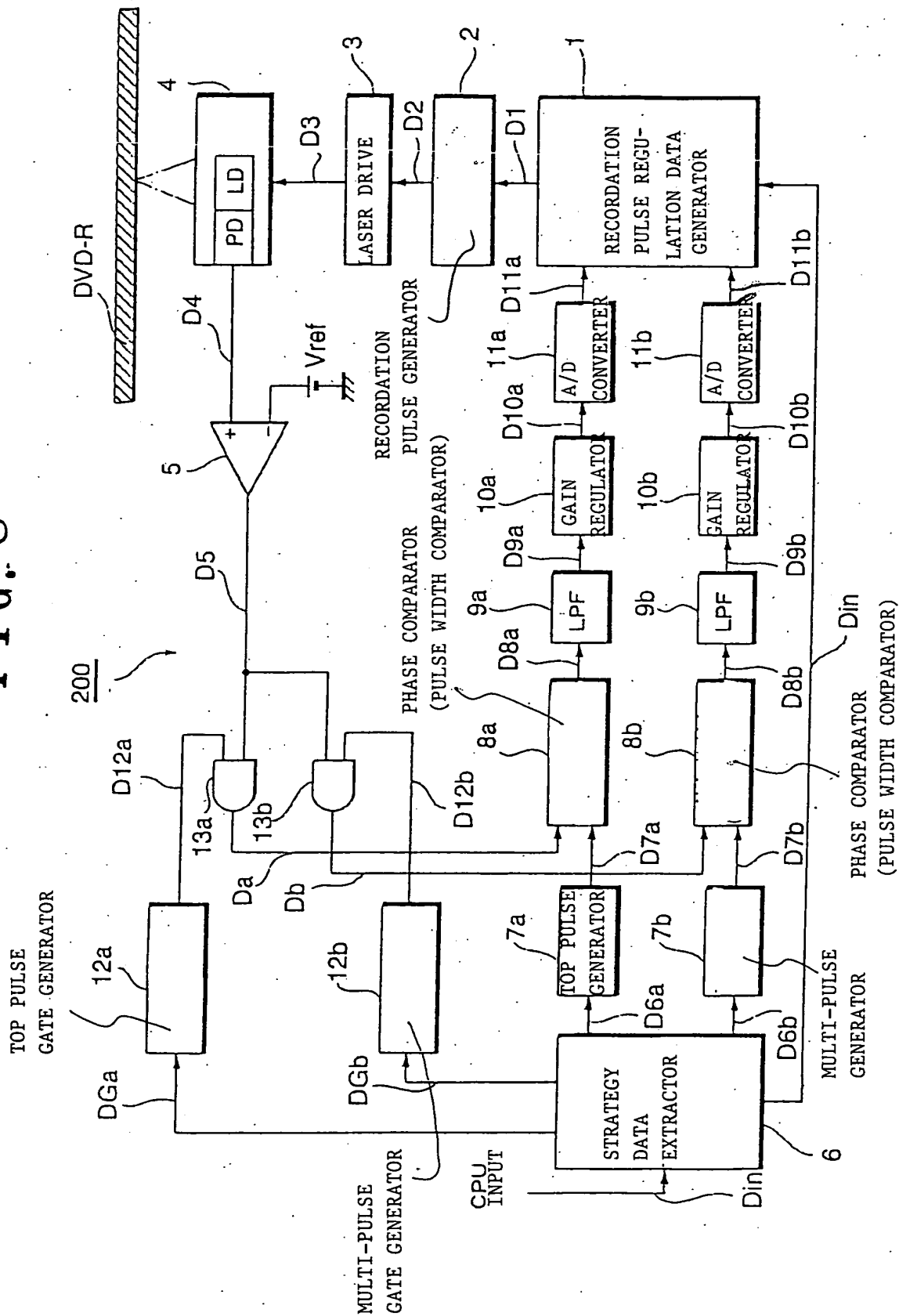


FIG. 7 B



"TOP PULSE" and "MULTI-PULSE"

FIG. 8



TOP PULSE
COMPONENTMULTI-PULSE
COMPONENTS

FIG. 9A

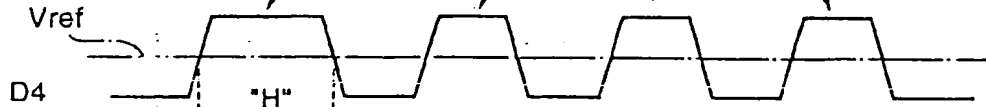


FIG. 9B



FIG. 9C

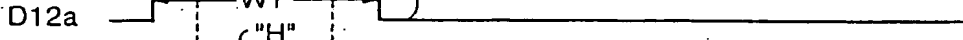


FIG. 9D

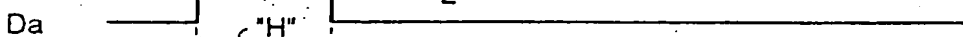


FIG. 9E

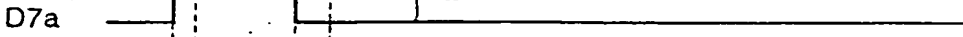


FIG. 9F



FIG. 9G

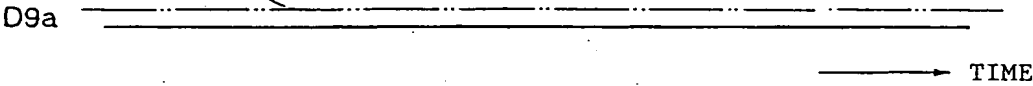


FIG. 10A

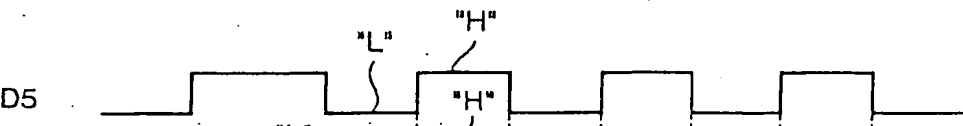


FIG. 10B

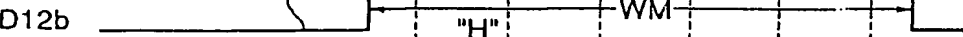


FIG. 10C

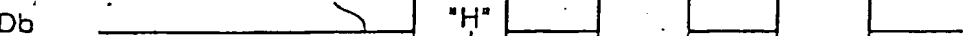


FIG. 10D

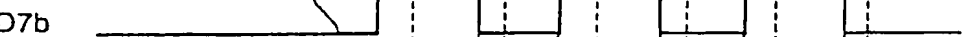


FIG. 10E

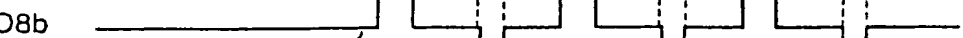


FIG. 10F

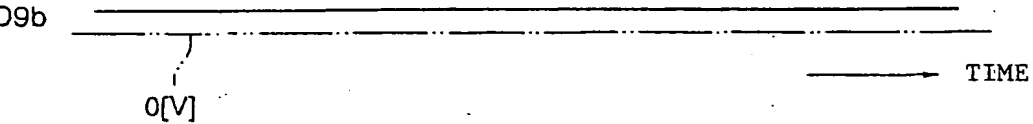
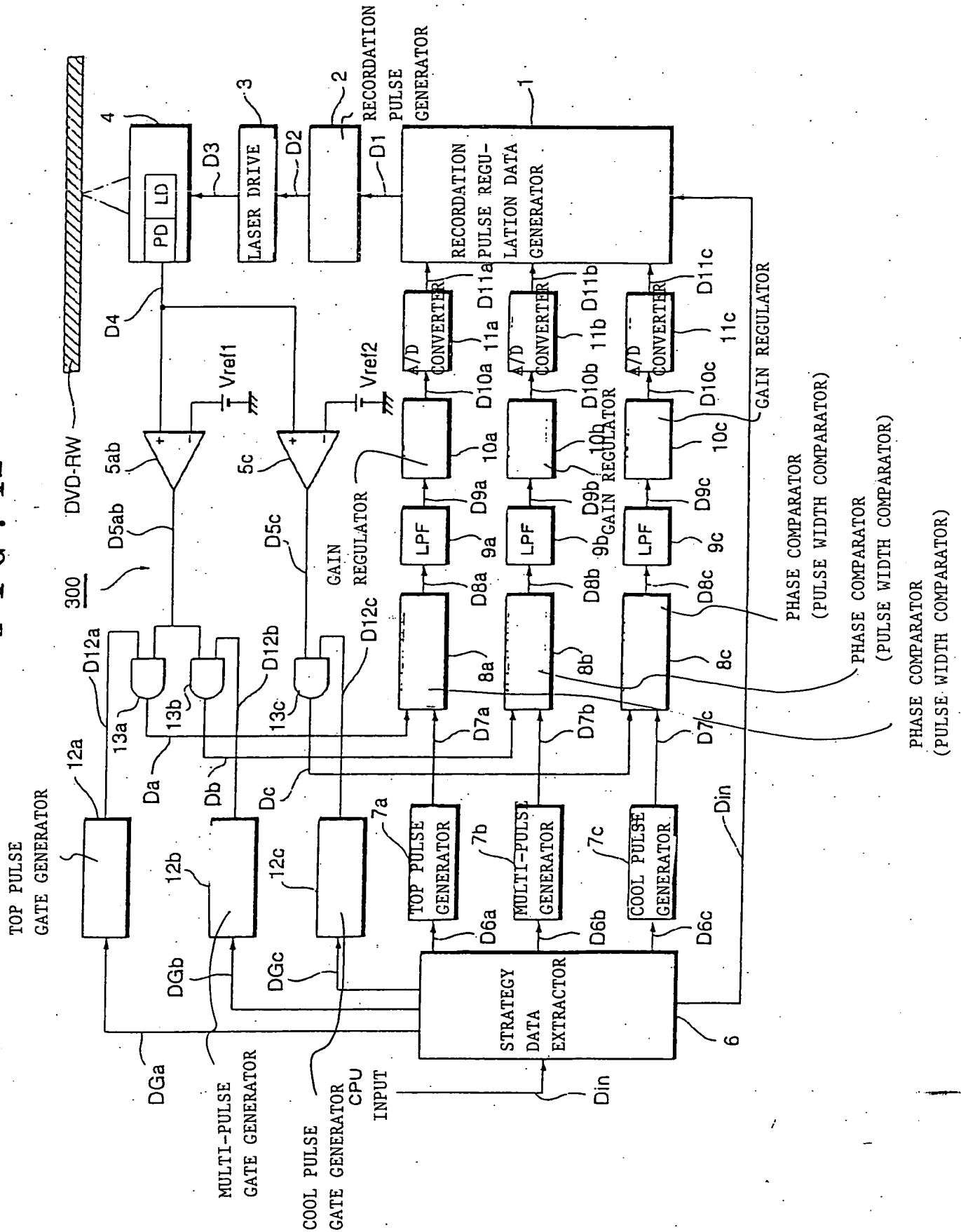


FIG. 11



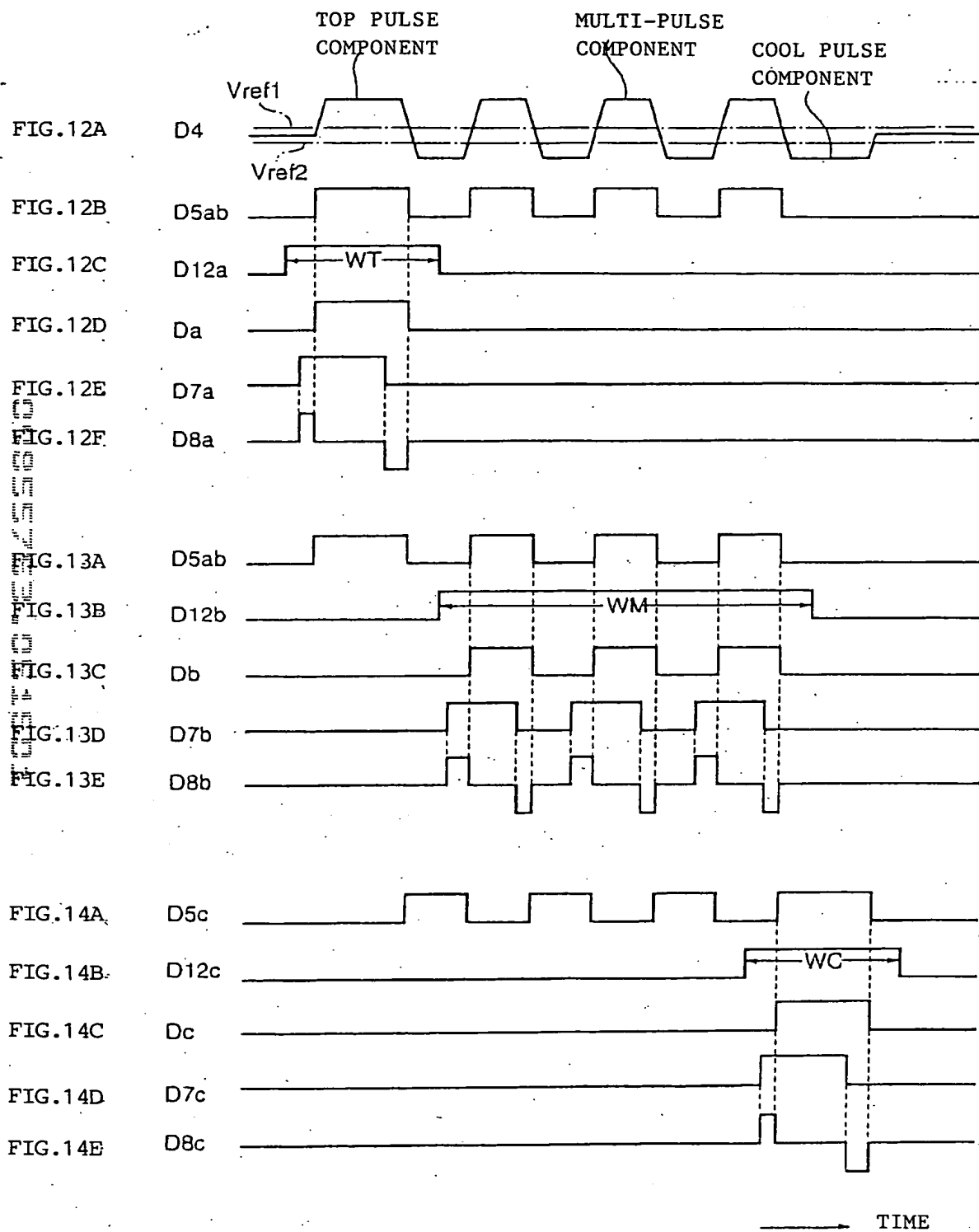


FIG.15A

FIG.15B

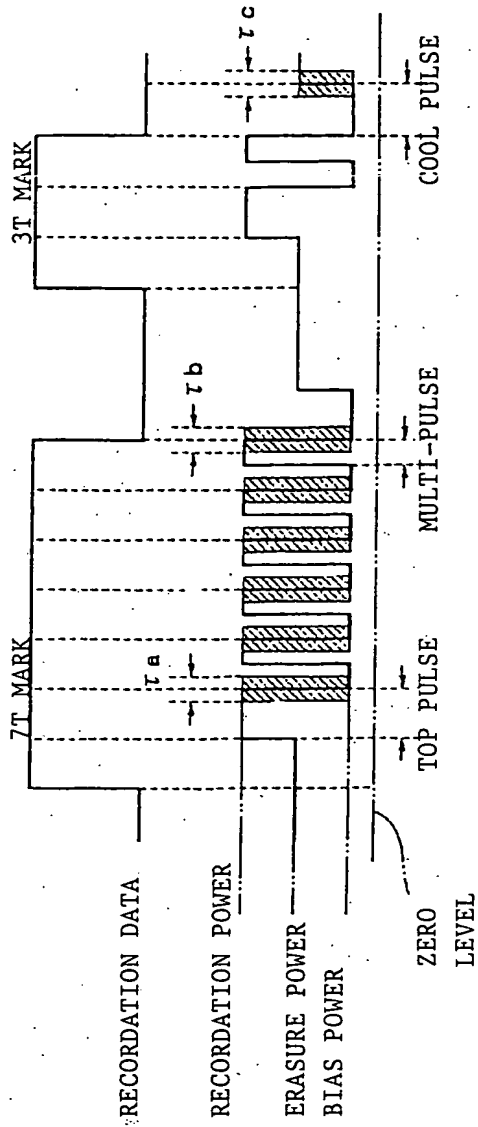




FIG. 16